EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	310	dithering adj2 (step module unit logic)	US-PGPUB; USPAT	OR	ON	2006/03/24 13:54
L2	2173	clock adj synchroniz$3	US-PGPUB	OR	ON	2006/03/24 13:54
L3	445	(phase adj lock) adj2 (circuit unit logic)	US-PGPUB	OR	ON	2006/03/24 13:55
L4	1	1 same 3	US-PGPUB	OR	ON	2006/03/24 13:55
L5	1	1 same 2	US-PGPUB	OR	ON	2006/03/24 13:56
L6	256	estimated adj rate	US-PGPUB	OR	ON	2006/03/24 13:56
L7	2	6 with transition	US-PGPUB	OR	ON	2006/03/24 13:56
L8	7063	adjust$3 adj2 frequency	US-PGPUB	OR	ON	2006/03/24 13:56
L9	5	8 with 2	US-PGPUB	OR	ON	2006/03/24 13:57
L10	1	8 with 6	US-PGPUB	OR	ON	2006/03/24 13:57
L11	2692	frequency near2 adjustment	US-PGPUB	OR	ON	2006/03/24 13:58
L12	233	generat$3 near10 11	US-PGPUB	OR	ON	2006/03/24 13:58
L13	9	12 with (input adj1 signal)	US-PGPUB	OR	ON	2006/03/24 13:58
L14	179	set$4 near8 11	US-PGPUB	OR	ON	2006/03/24 13:59
L15	0	14 with 1	US-PGPUB	OR	ON	2006/03/24 13:59
L18	813	synchronization adj1 error	US-PGPUB	OR	ON	2006/03/24 14:01
L19	42545	predetermined adj1 value	US-PGPUB	OR	ON	2006/03/24 14:01
L20	7	18 with 19	US-PGPUB	OR	ON	2006/03/24 14:03
L21	6241	exceed near12 (predetermined adj value)	US-PGPUB	OR	ON	2006/03/24 14:04
L22	2	21 with 18	US-PGPUB	OR	ON	2006/03/24 14:04
L24	5	(error adj1 test) adj1 module	US-PGPUB	OR	ON	2006/03/24 14:04
L25	17736	phase adj1 difference	US-PGPUB	OR	ON	2006/03/24 14:05
L26	138	25 with (clock near10 (input adj1 signal))	US-PGPUB	OR	ON	2006/03/24 14:06
L27	1	26 and 6	US-PGPUB	OR	ON	2006/03/24 14:06
L28	10805	error adj1 (detection module)	US-PGPUB	OR	ON	2006/03/24 14:06
L29	0	28 with 6	US-PGPUB	OR	ON	2006/03/24 14:07
L31	6	28 and 6	US-PGPUB	OR	ON	2006/03/24 14:07
L32	7	28 same 11	US-PGPUB	OR	ON	2006/03/24 14:07